

# Cassandra Plata

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**OBJECTIVE** Working towards extending my knowledge and skills in the field of Electrical Engineering through a full time position.

**EDUCATION** **Iowa State University – Ames, IA – Graduate May 2019**  
B.S. Electrical Engineering Cardinal Leadership Scholar GPA: 3.39

**WORK & PROJECTS** **Product Engineering Intern at Micron Technology - 2017 - 2018**

Non-Volatile NAND product engineering on the Probe Product Functional Team, and Device team

Created, ran, and documented test sequences to characterize parts of a system.

Analyze schematics to improve current test sequences.

Ran and debug simulations to ensure functionality of circuit and firmware

Created and presented firmware training for all of Non-Volatile Engineering

**Circuit Drawing and Simulating Website - 2018-Present**

Capstone project involving a team of 6 to create a user friendly circuit drawing website/application

**Electronic Theremin - 2018**

Designed and tuned an analog circuit to create an Electronic Theremin

Using only DC input, create an electronic instrument with variable pitch and volume

**Industrial Design Independent Study - 2017**

Study the learning/thought processes of non-engineers through shadowing third year industrial design studios

Create Arduino tutorials/activities catered toward non-engineers

**Basic Remote Controller - 2017**

Analog circuit of an infrared transmitter and receiver that emulates an optical signaling device

Work with 2 frequencies - 1 kHz and 10 kHz

**ACTIVITIES & LEADERSHIP**

- **Electrical Engineering Peer Mentor 2015 - Present**  
Lead labs and facilitate students' journey in learning how to be better problem solvers  
Continual development of methods in mentoring to encourage students to become critical thinkers
- **College of Electrical and Computer Engineering Student Ambassador - Present**  
Meet with prospective students and their families to discuss a future in the ECpE Department at Iowa State University
- **ISU Jazz Ensemble 2015-2017**
- **IEEE (Institute of Electrical and Electronics Engineering) 2015 - Present**
- **Broomball Intramural Co-Ed team 2015-Present**

**SKILLS**

- Experience with C, Arduino/Energia IDE, Java, Verilog, Perl, MATLAB
- PSpice, Cadence Schematic and Layout,
- Validation and Debug with simulation testing experience
- Understanding of fundamentals and qualification in analog and digital circuit design
- Proficient in Microsoft Word, Excel, and PowerPoint
- Strong communication, group planning, presentation, and leadership skill sets